



PJA3441-AU

40V P-Channel Enhancement Mode MOSFET

Voltage

-40 V

Current

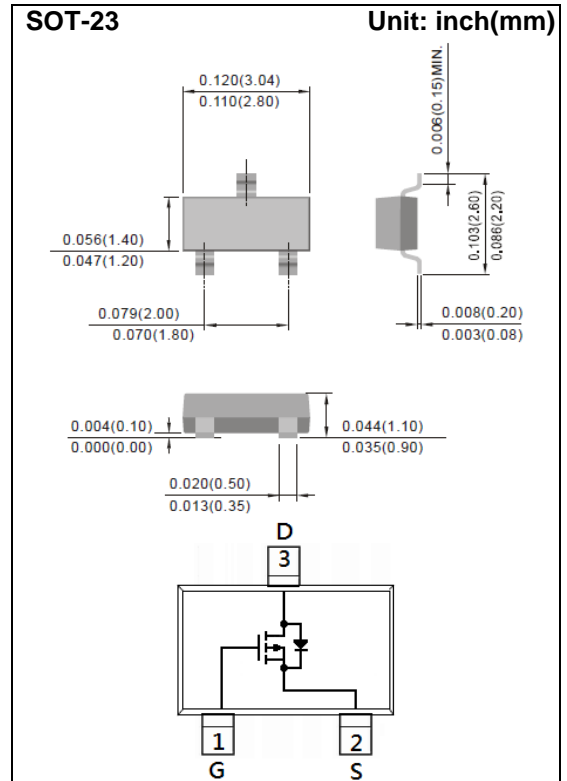
-3.1A

Features

- $R_{DS(ON)}$, $V_{GS}@-10V$, $I_D@-3.1A < 88m\Omega$
- $R_{DS(ON)}$, $V_{GS}@-4.5V$, $I_D@-2.6A < 108m\Omega$
- Advanced Trench Process Technology
- Specially Designed for Switch Load, PWM Application, etc
- AEC-Q101 qualified
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

Mechanical Data

- Case: SOT-23 Package
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.0003 ounces, 0.0084 grams



Maximum Ratings and Thermal Characteristics ($T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNITS
Drain-Source Voltage	V_{DS}	-40	V
Gate-Source Voltage	V_{GS}	+20	
Continuous Drain Current	I_D	-3.1	A
Pulsed Drain Current ^(Note 4)	I_{DM}	-12.4	
Power Dissipation	$T_a=25^\circ\text{C}$	1.25	W
	Derate above 25°C	10	mW/ $^\circ\text{C}$
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55~150	$^\circ\text{C}$
Typical Thermal Resistance	$R_{\theta JA}$	100	$^\circ\text{C/W}$
- Junction to Ambient ^(Note 3)			



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Electrical Characteristics (T_A=25 °C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =-250uA	-40	-	-	V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =-250uA	-1.0	-1.5	-2.5	
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =-10V, I _D =-3.1A	-	74	88	mΩ
		V _{GS} =-4.5V, I _D =-2.6A	-	88	108	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-40V, V _{GS} =0V	-	-	-1	uA
Gate-Source Leakage Current	I _{GSS}	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA
Dynamic (Note 5)						
Total Gate Charge	Q _g	V _{DS} =-20V, I _D =-3.1A, V _{GS} =-4.5V (Note 1,2)	-	6	-	nC
Gate-Source Charge	Q _{gs}		-	1.6	-	
Gate-Drain Charge	Q _{gd}		-	2.3	-	
Input Capacitance	C _{iss}	V _{DS} =-20V, V _{GS} =0V, f=1.0MHZ	-	505	-	pF
Output Capacitance	C _{oss}		-	48	-	
Reverse Transfer Capacitance	C _{rss}		-	33	-	
Turn-On Delay Time	t _{d(on)}	V _{DD} =-20V, I _D =-2.5A, V _{GS} =-10V, R _G =1Ω (Note 1,2)	-	6	-	ns
Turn-On Rise Time	t _r		-	35	-	
Turn-Off Delay Time	t _{d(off)}		-	18	-	
Turn-Off Fall Time	t _f		-	10	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I _S	---	-	-	-1.0	A
Diode Forward Voltage	V _{SD}	I _S =-1.0A, V _{GS} =0V	-	-0.82	-1.2	V
Reverse Recovery Time	t _{rr}	V _{GS} =0V, I _S =-2.5A di _F / dt=100A/us	-	13	-	ns
Reverse Recovery Charge	Q _{rr}		-	8.7	-	nC

NOTES:

1. Pulse width ≤ 300us, Duty cycle ≤ 2%
2. Essentially independent of operating temperature typical characteristics.
3. R_{θJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper.
4. The maximum current rating is package limited.
5. Guaranteed by design, not subject to production testing.



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TYPICAL CHARACTERISTIC CURVES

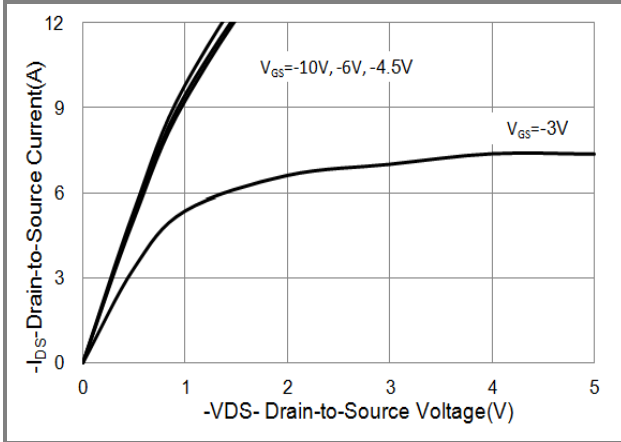


Fig.1 On-Region Characteristics

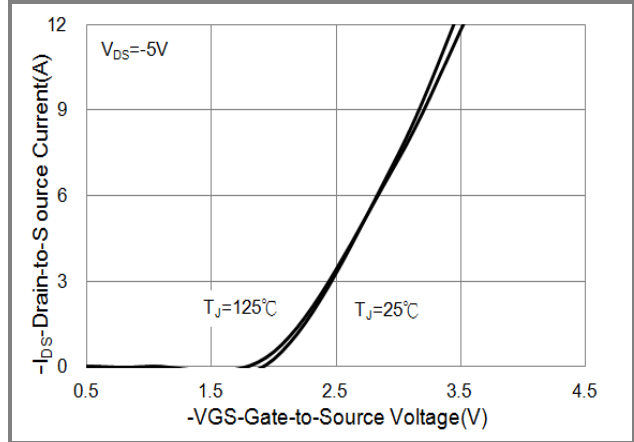


Fig.2 Transfer Characteristics

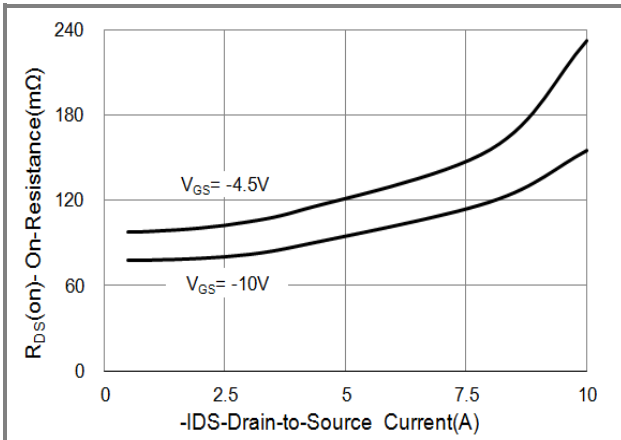


Fig.3 On-Resistance vs. Drain Current

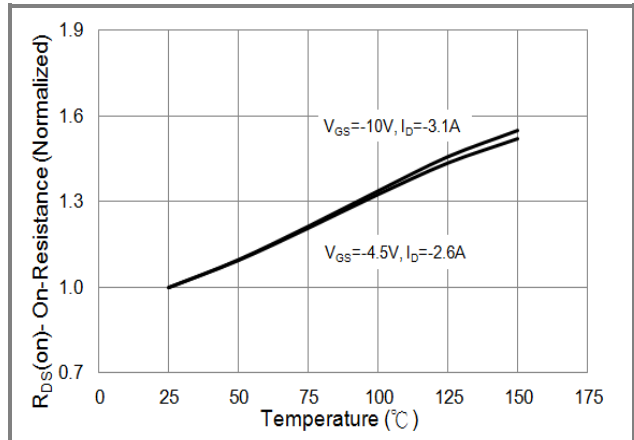


Fig.4 On-Resistance vs. Junction temperature

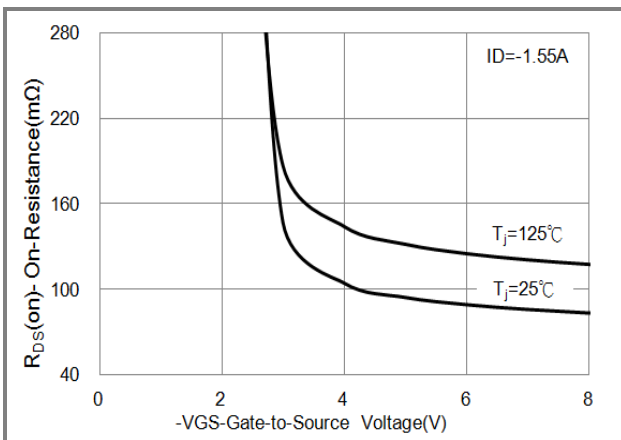


Fig.5 On-Resistance Variation with V_{GS}

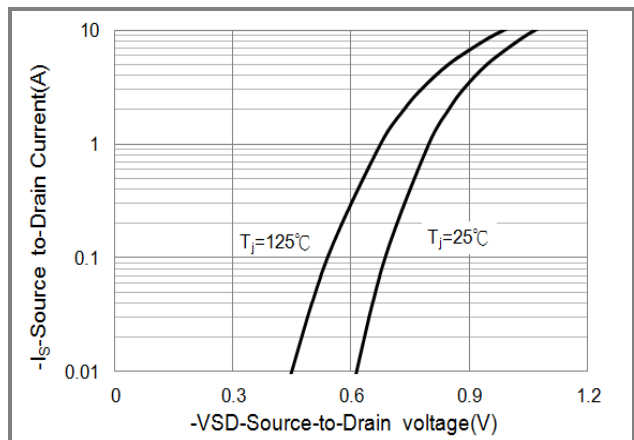


Fig.6 Body Diode Characteristics



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TYPICAL CHARACTERISTIC CURVES

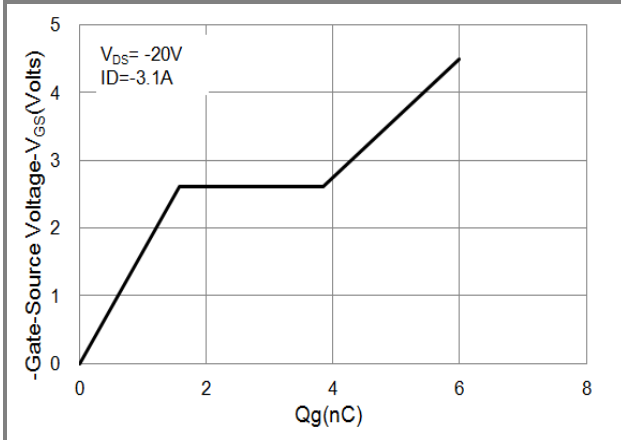


Fig.7 Gate-Charge Characteristics

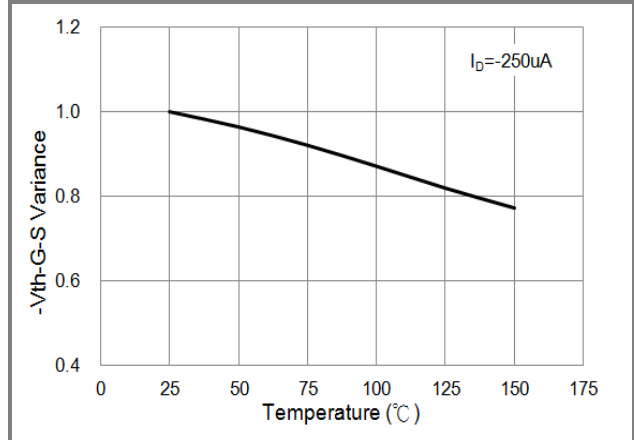


Fig.8 Threshold Voltage Variation with Temperature

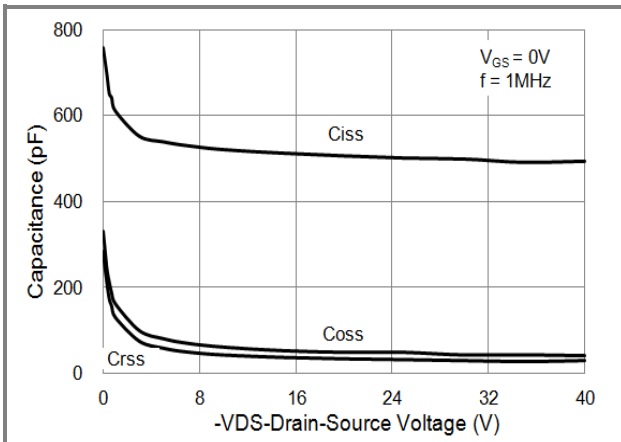


Fig.9 Capacitance vs. Drain-Source Voltage

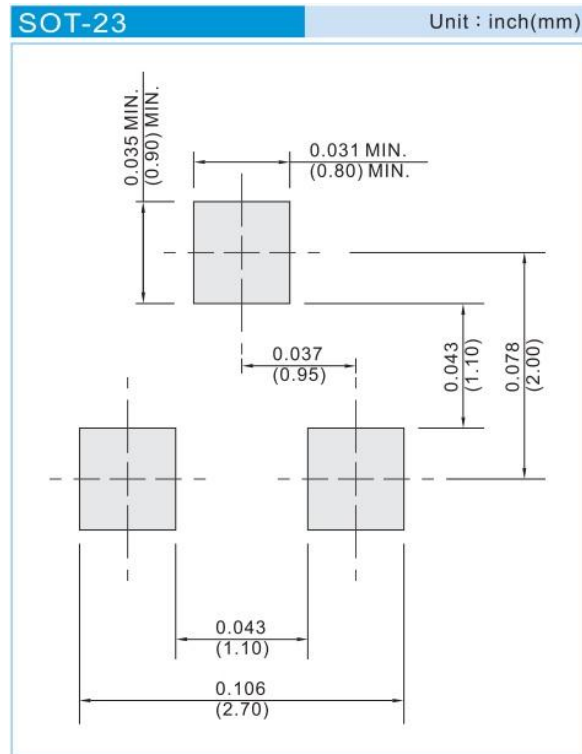


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Part No Packing Code Version

Part No Packing Code	Package Type	Packing Type	Marking	Version
PJA3441-AU_R1_000A1	SOT-23	3K pcs / 7" reel	A41	Halogen free

Mounting Pad Layout





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