



PJA7002H

60V N-Channel Enhancement Mode MOSFET

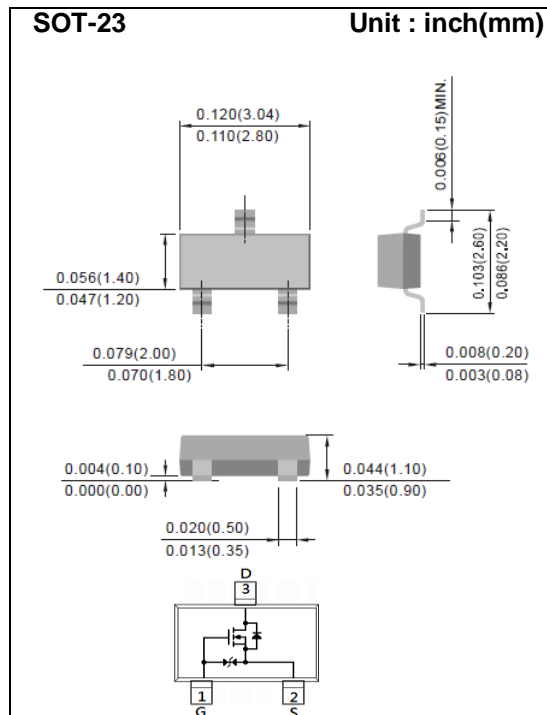
Voltage 60 V **Current** 300mA

Features

- RDS(ON) , VGS@10V, ID@300mA<5Ω
- RDS(ON) , VGS@5V, ID@50mA<7.5Ω
- Advanced Trench Process Technology
- ESD Protected
- Specially Designed for Relay driver, Speed line drive, etc.
- Lead free in compliance with EU RoHS 2011/65/EU directive.
- Green molding compound as per IEC61249 Std. (Halogen Free)

Mechanical Data

- Case: SOT-23 Package
- Terminals: Solderable per MIL-STD-750, Method 2026



Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNITS
Drain-Source Voltage	V _{DS}	60	V
Gate-Source Voltage	V _{GS}	+20	V
Continuous Drain Current	I _D	300	mA
Pulsed Drain Current	I _{DM}	1200	mA
Power Dissipation	P _D	T _A =25°C	500
		Derate above 25°C	4
Operating Junction and Storage Temperature Range	T _J , T _{STG}	-55~150	°C
Typical Thermal Resistance	R _{θJA}	250	°C/W
- Junction to Ambient (Note 3)			



PJA7002H

Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static (Note 1)						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250uA	60	-	-	V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250uA	2.0	2.49	3.0	V
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =10V, I _D =300mA	-	2.0	5	Ω
		V _{GS} =5V, I _D =50mA	-	3.6	7.5	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =60V, V _{GS} =0V	-	-	1	uA
Gate-Source Leakage Current	I _{GSS}	V _{GS} =±20V, V _{DS} =0V	-	-	±0.5	uA
Dynamic (Note 4)						
Total Gate Charge	Q _g	V _{DS} =10V, I _D =300mA, V _{GS} =4.5V (Note 1,2)	-	1.3	-	nC
Gate-Source Charge	Q _{gs}		-	0.6	-	
Gate-Drain Charge	Q _{gd}		-	0.2	-	
Input Capacitance	C _{iss}	V _{DS} =25V, V _{GS} =0V, f=1.0MHZ	-	22	-	pF
Output Capacitance	C _{oss}		-	12	-	
Reverse Transfer Capacitance	C _{rss}		-	1.7	-	
Turn-On Delay Time	t _{d(on)}	V _{DD} =10V, I _D =300mA, V _{GS} =10V, R _G =10Ω (Note 1,2)	-	2.9	-	ns
Turn-On Rise Time	t _r		-	1.8	-	
Turn-Off Delay Time	t _{d(off)}		-	5.6	-	
Turn-Off Fall Time	t _f		-	1.9	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I _S	---	-	-	300	mA
Diode Forward Voltage	V _{SD}	I _S =300mA, V _{GS} =0V	-	0.92	1.5	V

NOTES :

1. Pulse width ≤ 300us, Duty cycle ≤ 2%
2. Essentially independent of operating temperature typical characteristics.
3. R_{θJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. mounted on a 1 inch square pad of copper
4. Guaranteed by design, not subject to production testing



PJA7002H

TYPICAL CHARACTERISTIC CURVES

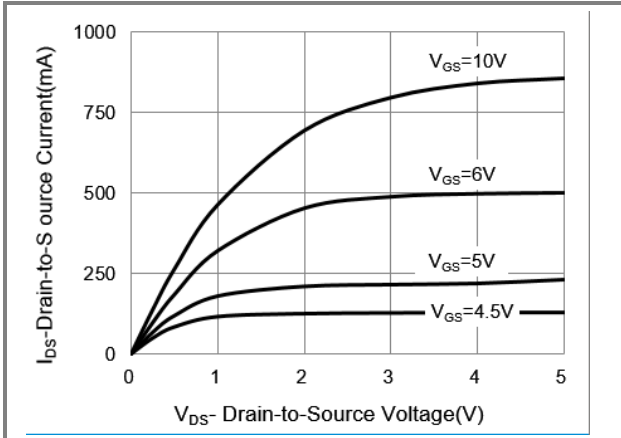


Fig.1 On-Region Characteristics

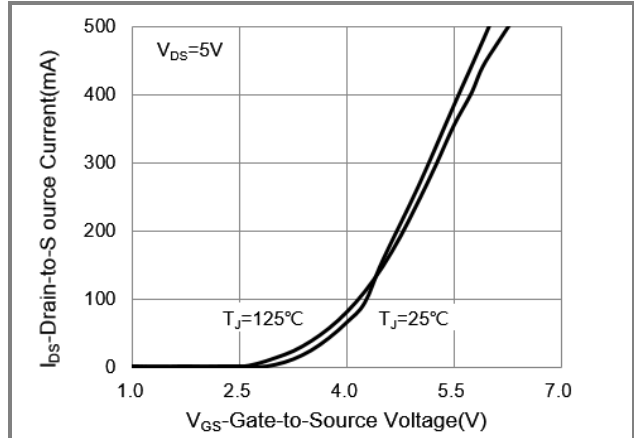


Fig.2 Transfer Characteristics

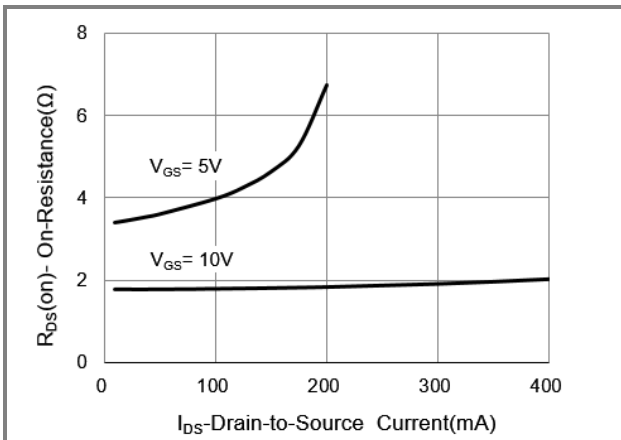


Fig.3 On-Resistance vs. Drain Current

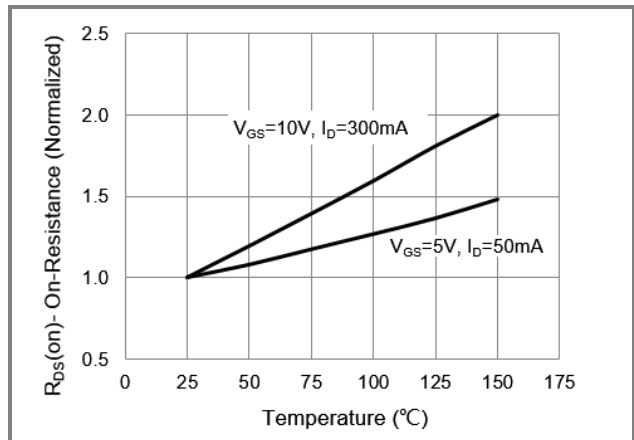


Fig.4 On-Resistance vs. Junction temperature

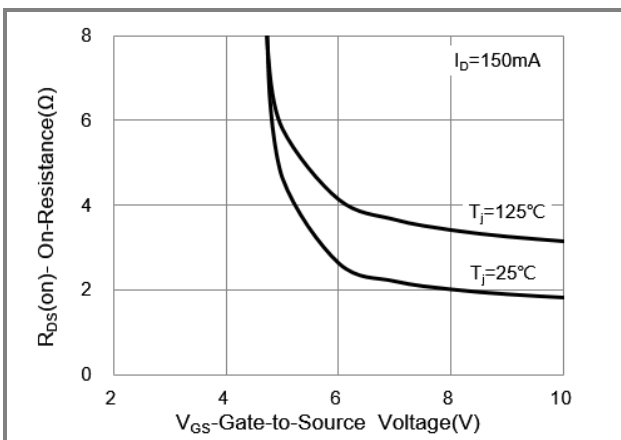


Fig.5 On-Resistance Variation with VGS.

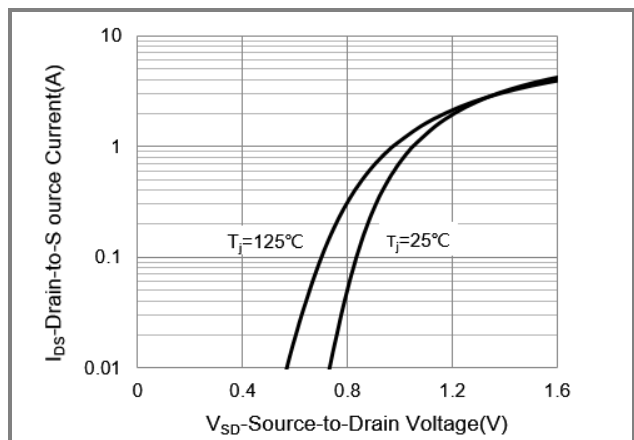


Fig.6 Body Diode Characteristics



PJA7002H

TYPICAL CHARACTERISTIC CURVES

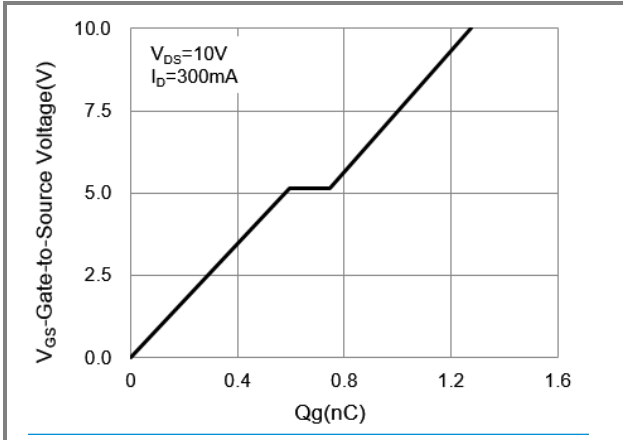


Fig.7 Gate-Charge Characteristics

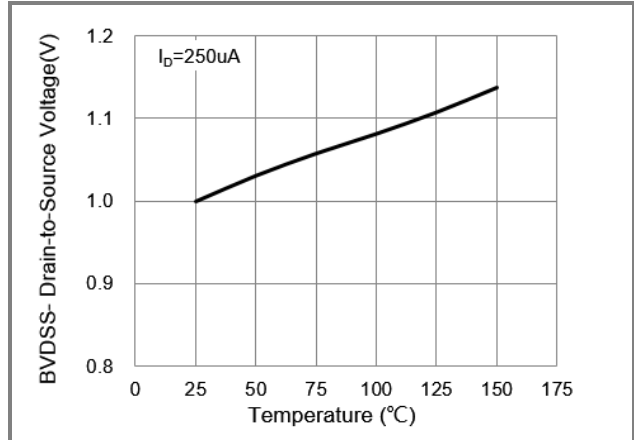


Fig.8 Breakdown Voltage Variation vs. Temperature

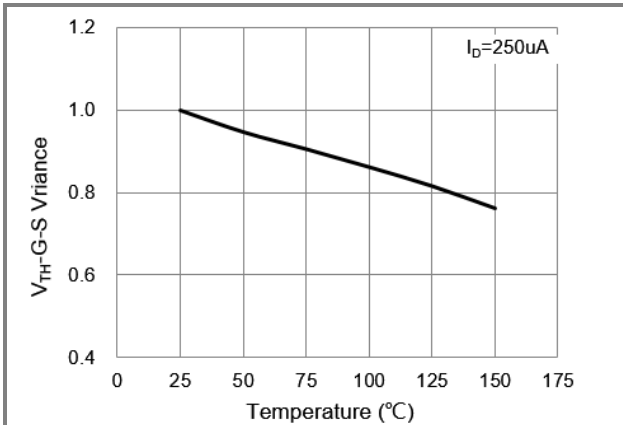


Fig.9 Threshold Voltage Variation with Temperature.

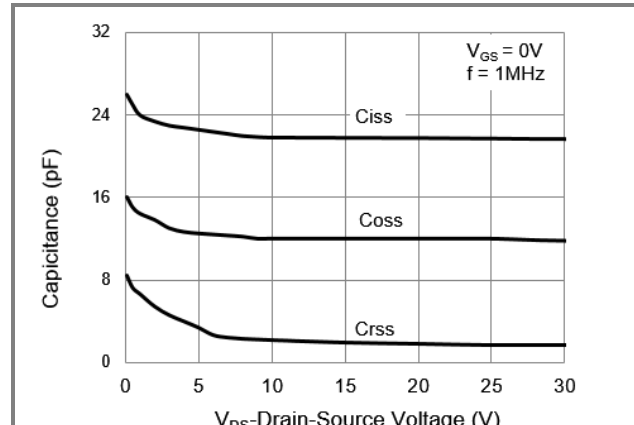


Fig.10 Capacitance vs. Drain-Source Voltage.

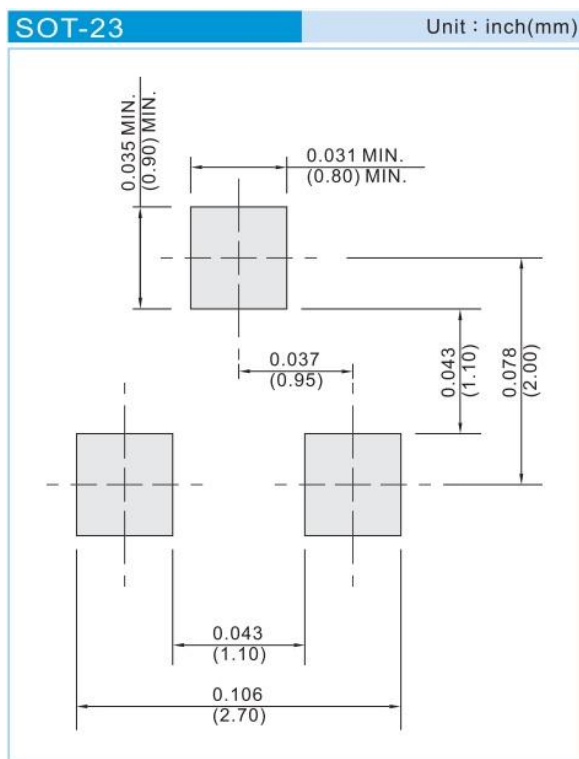


PJA7002H

PART NO PACKING CODE VERSION

PART NO PACKING CODE	Package Type	Packing Type	Marking	Version
PJA7002H_R1_00001	SOT-23	3K pcs / 7" reel	A2H	Halogen free
PJA7002H_R2_00001	SOT-23	12K pcs / 13" reel	A2H	Halogen free

MOUNTING PAD LAYOUT





PJA7002H

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